

Figure 1 Distributed Hardware Showing Interrelationship between Client and Server

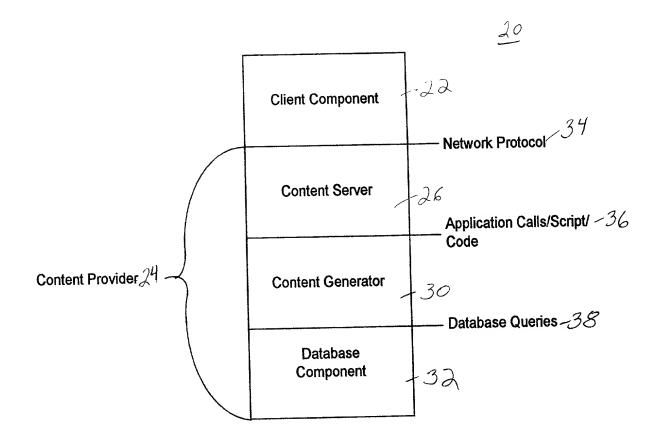


Figure 2 Block Diagram

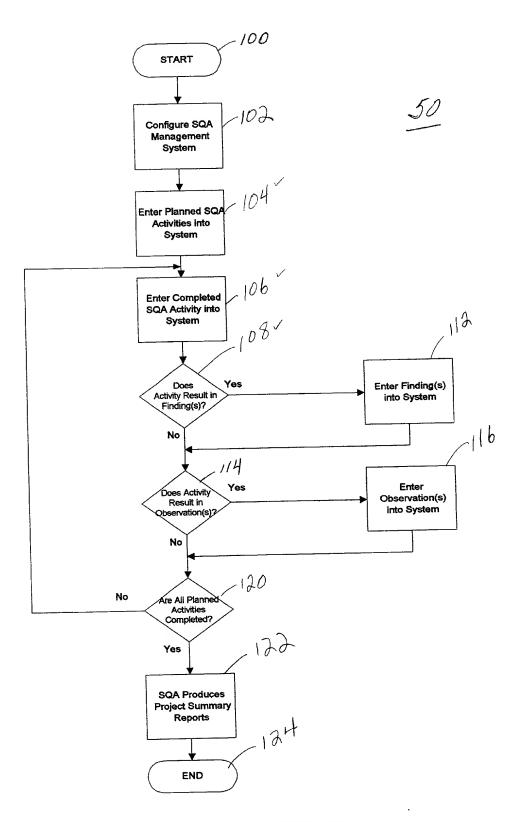


Figure 3 High Level SQA Management System Process

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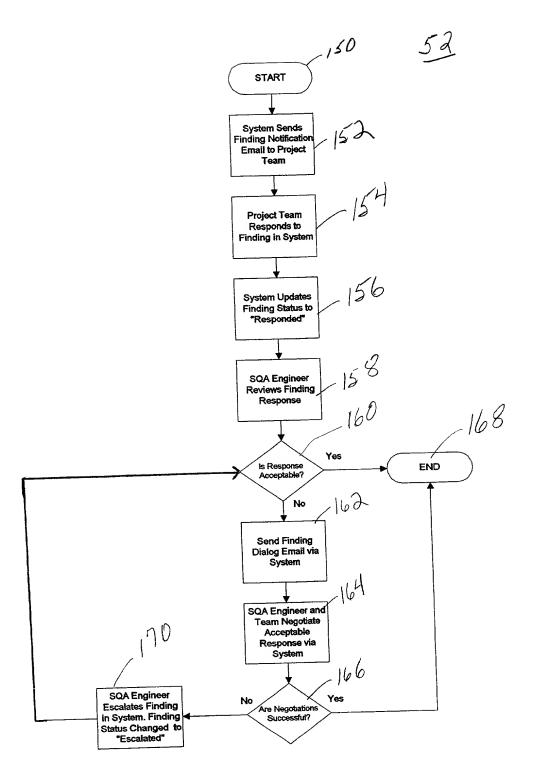


Figure 4 Finding Notification and Response Process

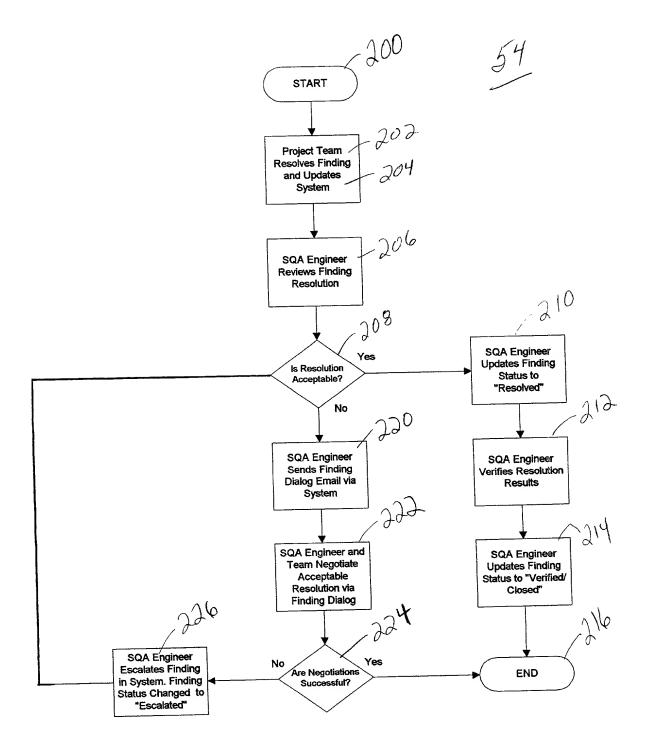


Figure 5 Finding Resolution, Verification and Closure Process

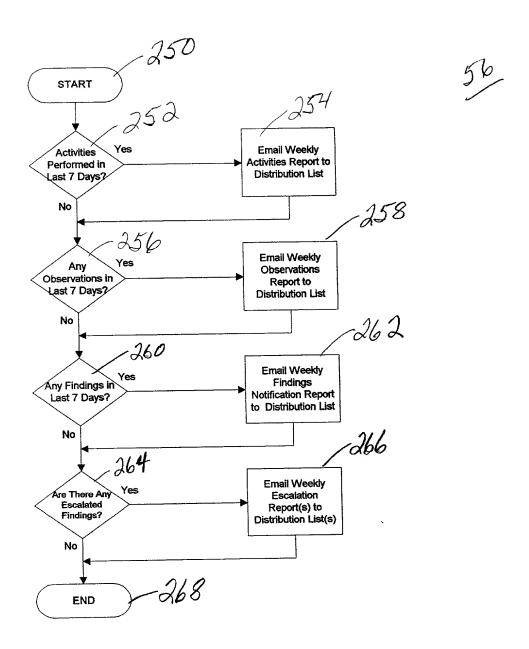


Figure 6 Weekly Reporting Process

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